

Program the resulting configuration onto the target CCGx/PAG1S device. The utility works with the Infineon supplied hardware which hosts the CCGx controllers along with a USB interface. Note that ...

The WiFi LoRa 32 V4, the next gen to the Heltec V3, is a powerful, high-efficiency IoT maker board featuring an ESP32S3 MCU and SX1262 LoRa Node chip; retaining the V3's size and identical ...

FlashPro Express is used by Libero to perform the programming tasks for PolarFire, PolarFire SoC, SmartFusion2, IGLOO2, and RTG4, as part of the design flow. Stand-alone. FlashPro Express is ...

These labs introduce the AMD Vivado(TM) Design Suite debug methodology recommended to debug your FPGA designs. The labs describe the steps involved in taking a small RTL design and the multiple ...

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The first step is to check the "Versal Configuration Memory Devices" table in UG908. If your OSPI flash device is listed, then the device is supported by AMD Vivado software and you can proceed to the ...

The device uses normal Cortex-M reset, no special handling necessary, like described here.

Flashing the firmware through the SWD lines should not cause any issues, as long as the connection is correct. If you're able to load the bootloader, it's likely that the connection is good and ...

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Make sure to remove the connector gradually to prevent excessive stress on the fiber). OSFP-SR4-400G-FL MTP Patch Cord Removal OSFP-SR8-800G MTP Patch Cord Removal Copyright © 2024 ...

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